Cover Page

EE 316-08

Electric Circuits & Electronics Design Lab

**Lab 1: Circuits Review**

**By**: Nolan Anderson

**Lab Date: 01/26/2021**

**Lab Due: 01/26/2021**

**1. Introduction:**

This lab simply covers the basics and reviews Ohm’s law, and Kirchhoff’s current and voltages laws. It also dives into using Thevenin Equivalent equations to solve a given circuit. Continually, this lab is pivotal in learning Multisim and understanding how to use its features to assist us in our lab.

**2. Theoretical Analysis:**

Using figure 1 below from the lab manual, we use mesh analysis to solve for branch currents, node voltages, loop currents, and branch voltages. We initially calculate them with the values as shown:

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| Vs = 5.0v  R1 = R2 = R3 = 100 ohms  R4 = R5 = 1000 ohms  R6 = R7 = 2200 ohms |

After we run through the calculations the first time, we then go back and run them with a 10% increase on the resistors, and again with a 10% decrease on the resistors. The figure for calculation and tables with results follow:

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Figure 1: Circuit under analysis.

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| |  |  |  |  |  | | --- | --- | --- | --- | --- | | **Branch Node or Loop #** | **Branch Voltages (V)** | **Branch Currents (mA)** | **Node Voltages (V)** | **Loop Currents (mA)** | | **1** | **0.363** | **3.634** | **4.637** | **3.634** | | **2** | **0.272** | **2.719** | **4.545** | **1.650** | | **3** | **0.092** | **0.915** | **4.365** | **0.915** | | **4** | **0.915** | **0.915** | **3.630** |  | | **5** | **0.735** | **0.735** |  |  | | **6** | **4.365** | **1.984** |  |  | | **7** | **3.630** | **1.650** |  |  | |

Table 1: Calculations with Resistors at Nominal.

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
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| |  |  |  |  |  | | --- | --- | --- | --- | --- | | **Branch Node or Loop #** | **Branch Voltages (V)** | **Branch Currents (mA)** | **Node Voltages (V)** | **Loop Currents (mA)** | | **1** | **0.363** | **4.038** | **4.637** | **4.038** | | **2** | **0.272** | **3.021** | **4.545** | **1.833** | | **3** | **0.092** | **1.017** | **4.365** | **1.017** | | **4** | **0.915** | **1.017** | **3.630** |  | | **5** | **0.734** | **0.816** |  |  | | **6** | **4.366** | **2.205** |  |  | | **7** | **3.629** | **1.833** |  |  | |

Table 2: Calculations with a 10% decrease on all resistors.

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
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| |  |  |  |  |  | | --- | --- | --- | --- | --- | | **Branch Node or Loop #** | **Branch Voltages (V)** | **Branch Currents (mA)** | **Node Voltages (V)** | **Loop Currents (mA)** | | **1** | **0.363** | **3.304** | **4.637** | **3.304** | | **2** | **0.272** | **2.472** | **4.545** | **1.500** | | **3** | **0.092** | **0.832** | **4.365** | **0.832** | | **4** | **0.915** | **0.832** | **3.630** |  | | **5** | **0.735** | **0.668** |  |  | | **6** | **4.366** | **1.804** |  |  | | **7** | **3.630** | **1.500** |  |  | |

Table 3: Calculations with a 10% increase on all resistors.

**3. Simulations:**

The following simulations were all performed in multisim. This will not only prove our findings from our analysis of Figure one, but we will also develop more designs for Thevenin equivalent circuits and prove ohms law.

First, I will provide the simulation for figure 1 in terms of voltages and currents. I will place my findings in tables 4-6.

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Figure 2: Simulation for figure 1, voltages

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Figure 3: Simulation for figure 1, amperes

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| |  |  |  |  |  | | --- | --- | --- | --- | --- | | **Branch Node or Loop #** | **Branch Voltages (V)** | **Branch Currents (mA)** | **Node Voltages (V)** | **Loop Currents (mA)** | | **1** | **0.363** | **3.634** | **4.637** | **3.634** | | **2** | **0.272** | **2.719** | **4.545** | **1.650** | | **3** | **0.092** | **0.915** | **4.365** | **0.915** | | **4** | **0.915** | **0.915** | **3.630** |  | | **5** | **0.735** | **0.735** |  |  | | **6** | **4.365** | **1.984** |  |  | | **7** | **3.630** | **1.650** |  |  | |

Table 4: Calculations with Resistors at Nominal.

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
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| |  |  |  |  |  | | --- | --- | --- | --- | --- | | **Branch Node or Loop #** | **Branch Voltages (V)** | **Branch Currents (mA)** | **Node Voltages (V)** | **Loop Currents (mA)** | | **1** | **0.363** | **4.038** | **4.637** | **4.038** | | **2** | **0.272** | **3.021** | **4.545** | **1.833** | | **3** | **0.092** | **1.017** | **4.365** | **1.017** | | **4** | **0.915** | **1.017** | **3.630** |  | | **5** | **0.734** | **0.816** |  |  | | **6** | **4.366** | **2.205** |  |  | | **7** | **3.629** | **1.833** |  |  | |

Table 5: Calculations with a 10% decrease on all resistors.

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
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| |  |  |  |  |  | | --- | --- | --- | --- | --- | | **Branch Node or Loop #** | **Branch Voltages (V)** | **Branch Currents (mA)** | **Node Voltages (V)** | **Loop Currents (mA)** | | **1** | **0.363** | **3.304** | **4.637** | **3.304** | | **2** | **0.272** | **2.472** | **4.545** | **1.500** | | **3** | **0.092** | **0.832** | **4.365** | **0.832** | | **4** | **0.915** | **0.832** | **3.630** |  | | **5** | **0.735** | **0.668** |  |  | | **6** | **4.366** | **1.804** |  |  | | **7** | **3.630** | **1.500** |  |  | |

Table 6: Calculations with a 10% increase on all resistors.

As you can see, comparing the data from tables 1-3 and 4-6 shows that my hand calculations were accurate. You can see the handwritten portion in the appendix. Next, we will consider the circuit as shown in Figure 5. Building this in Multisim (see figure 6), we can see the results in table 7.

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Figure 5: Test Circuit 1.4

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Figure 6: Figure 5 in Multisim

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| |  |  |  | | --- | --- | --- | | **RL** | **VL** | **IL A** | | **10** | **0.455** | **0.045** | | **20** | **0.833** | **0.042** | | **40** | **1.429** | **0.036** | | **70** | **2.059** | **0.029** | | **100** | **2.500** | **0.025** | | **150** | **3.000** | **0.020** | | **200** | **3.333** | **0.017** | | **300** | **3.750** | **0.013** | | **500** | **4.167** | **0.008** | | **1000** | **4.545** | **0.005** | |

Table 1.7: Multisim data from the Multisim figure 6.

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Plot 1: Voltage v Current for figure 6.

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Figure 6: Test Circuit 1.5

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Figure 7: Figure 6 in Multisim, voltages and current calculation.

Using the calculations from figure 7, we can see that the Thevenin equivalent resistance for figure 8 will be 1.5kohm

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Figure 8: Test Circuit 1.6

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Figure 9: Figure 8 in Multisim, Thevenin equivalent circuit for the calculations from Figure 7.

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Figure 10: Test Circuit 1.7

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Figure 11: Figure 10 in Multisim, Norton equivalent circuit.

**~~4. Experimental:~~**

We were not instructed to provide experimental results for this lab, see the following screenshot.

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**5. Results and Discussion:**

This lab set out to introduce, review, and prove Ohm’s law, Kirchoff’s Current Law, and Kirchoff’s voltage law. First, we look at figure 5 which covers ohm’s law. This law essentially states that the relationship between current and voltage is linear, see Plot 1. Next, we look at Kirchoff’s current and voltage laws both represented and proven by our first circuit displayed in figure 1. If you sum all the voltages in the closed loop circuit, within a small margin it sums to zero. Similarly, for the current law, we can see that they also sum to zero when looking at the nodes. Lastly, we used Thevenin and Norton circuits that provided us with similar results to their much more complicated circuits they were emulating.

**6. Conclusion:**

The findings from this lab helped to prove Kirchoff’s voltage and current laws as well as ohm’s law. Also, the use of Thevenin and Norton circuits makes solving a very complex circuit much easier, even in Multisim. Continually, this lab was extremely helpful in providing an introduction into Multisim and other tools that will be helpful in the long run. Lastly, from using Multisim to give us accurate results to solving the circuits by hand, this lab followed through on its purpose.

**7. Appendix:**

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Figure A1: Hand calculations for solving Figure 1.